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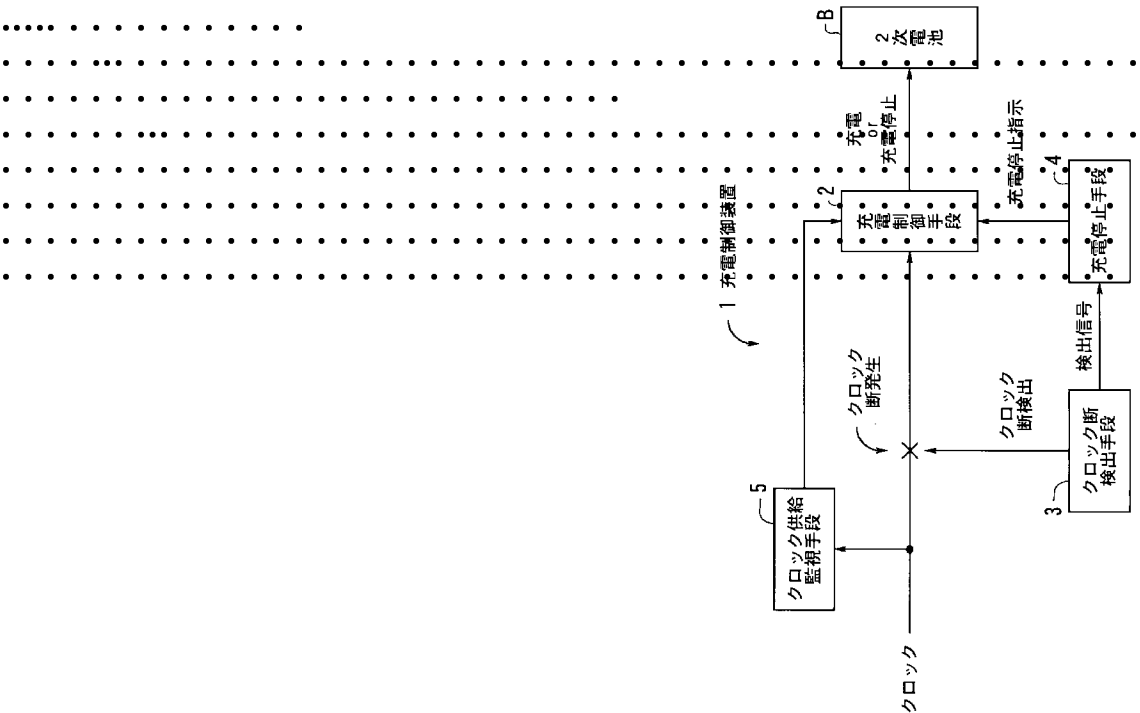
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(54)【発明の名称】 充電制御装置

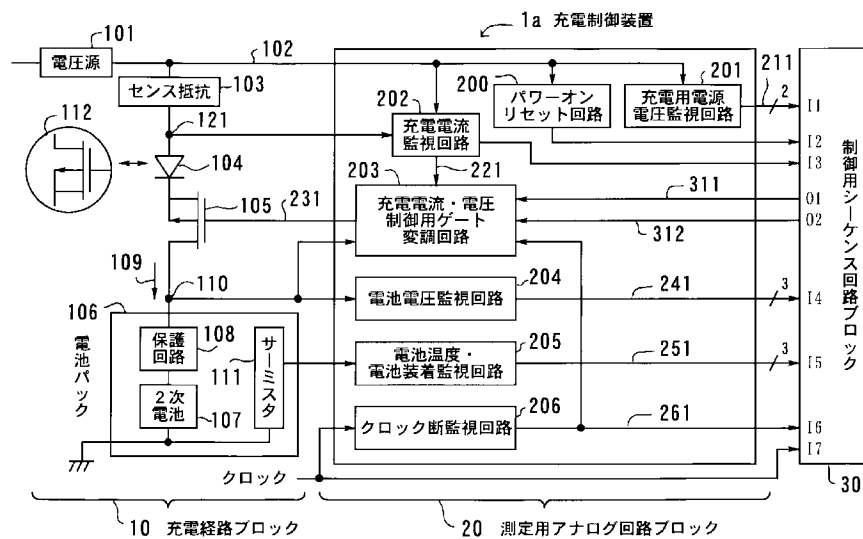


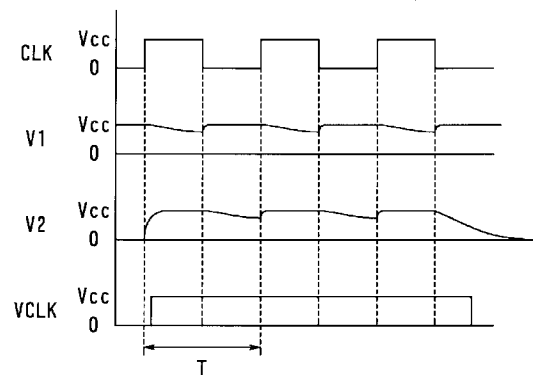
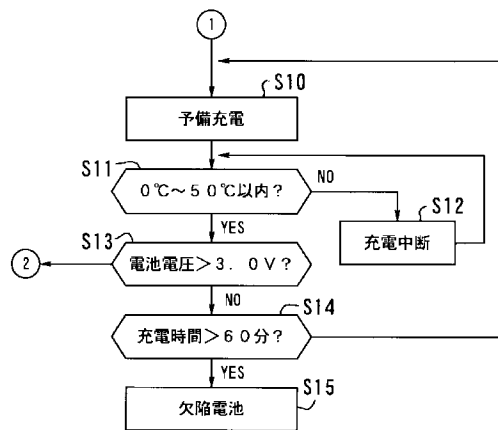
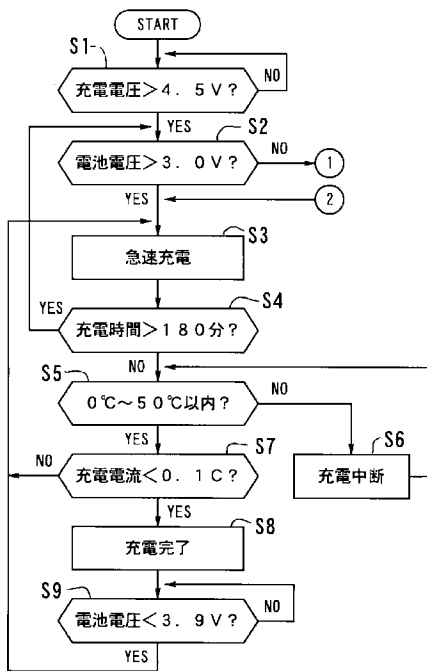
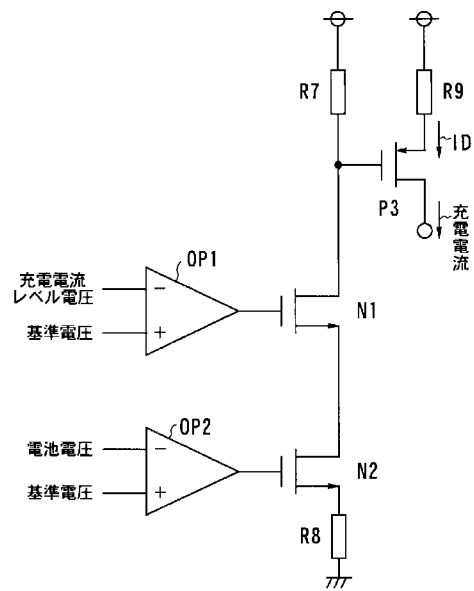
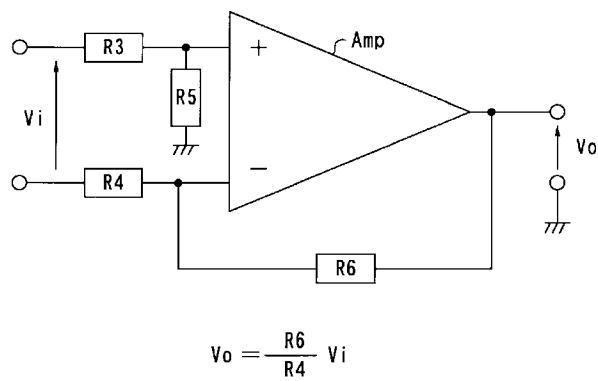
A large grid of dots is shown, with labels 10, 20, 30, 40, and 50 placed at regular intervals along the horizontal axis. The dots are arranged in a regular grid pattern, and the labels are placed below the grid.

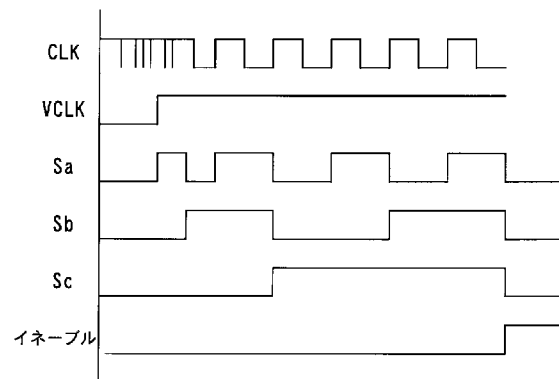
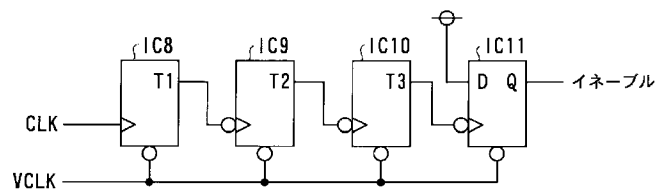
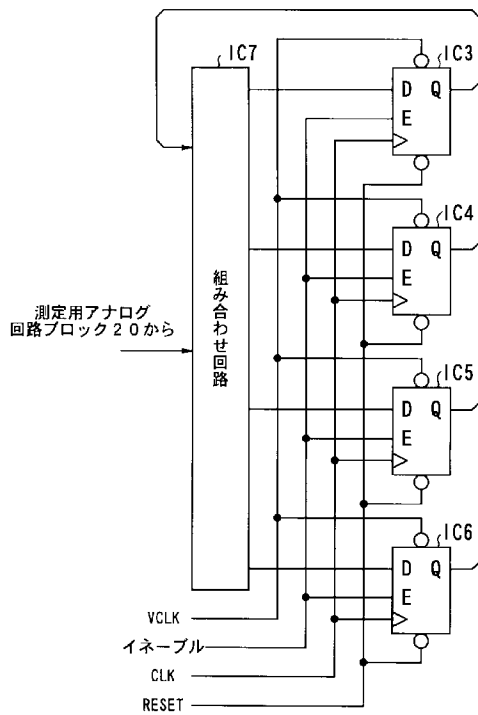
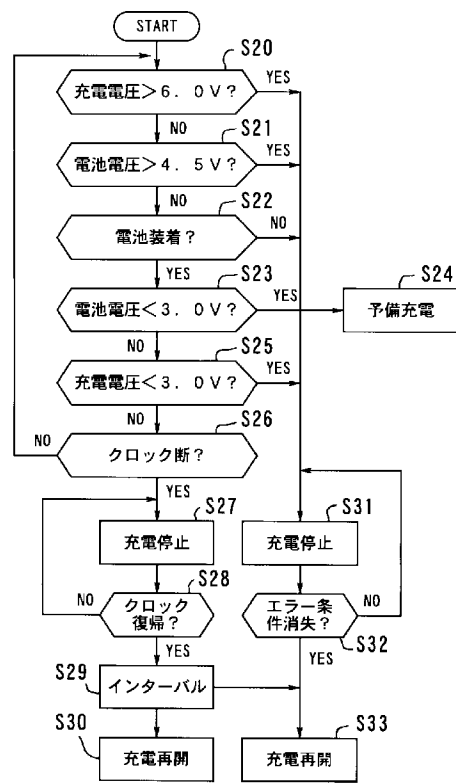
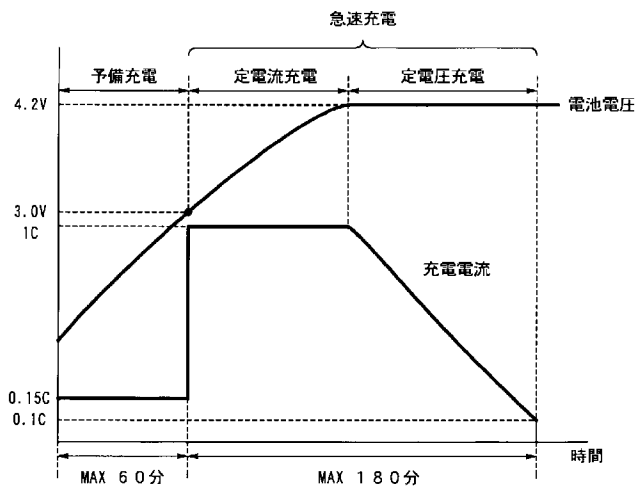
A dot grid paper template with a 20x20 grid of dots. The grid is divided into four quadrants by a vertical line and a horizontal line. The horizontal line is labeled "10" on the left and "10" on the right. The vertical line is labeled "10" at the top and "10" at the bottom. The bottom-left quadrant is labeled "30" at the bottom. The bottom-right quadrant is labeled "40" at the bottom. The bottom-left quadrant is also labeled "50" at the bottom. The bottom-right quadrant is also labeled "50" at the bottom.

A dot grid paper template with a 20x20 grid of dots. The grid is divided into four quadrants by a vertical line at column 10 and a horizontal line at row 10. The quadrants are labeled: top-left '10 10', top-right '10 10', bottom-left '20', and bottom-right '30 40 50'. The labels are placed at the intersections of the grid lines.

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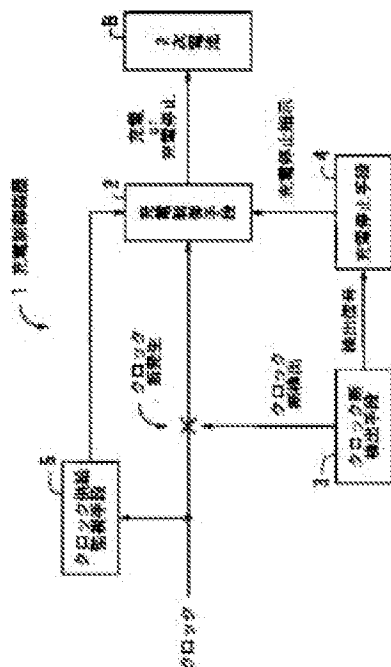
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(21)Application number : 2000-180827 (71)Applicant : FUJI ELECTRIC CO LTD

(22)Date of filing : 16.06.2000 (72)Inventor : YOSHIDA YUTAKA

(54) CHARGE CONTROLLER



(57)Abstract:

PROBLEM TO BE SOLVED: To prevent malfunctions in clock interruption for the enhancement of the safety and quality of charging operation.

SOLUTION: A charge controlling means 2 logically controls charging of a secondary battery B. A clock interruption detecting means 3 detects any clock interruption in clocks inputted to the charge controlling means 2 and generates a detection signal if any clock interruption occurs. A charge stopping means 4 stops charging operation based on the detection signal.